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FACSIMILE COVER SHEET

October 18, 2004

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Examiner: Edward Raymond Phone

Phone No.: (703) 308-6235 Fax No.: (703) 872-9306

From: Brian M. Dugan

Our File No.: Docket No. BUR920010034U52

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Leendert M. Huisman, Franco Motika, and

Leah M. Pfeifer Pastel, and William V.

Huott

Serial No. : 10/697,365

Filed: October 30, 2003

For : USING CLOCK GATING OR SIGNAL GATING TO

PARTITION A DEVICE FOR FAULT ISOLATION

AND DIAGNOSTIC DATA COLLECTION

Examiner : Edward Raymond

Group Art Unit: 2857

TOTAL NUMBER OF PAGES INCLUDING THIS PAGE:

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Docket No. BUR920010034US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Re:

Inventor(s):

Leendert M. Huisman, Franco Motika, Leah M. Pfeifer Pastel and William V. Huott

Title:

USING CLOCK GATING OR SIGNAL GATING TO PARTITION A DEVICE FOR FAULT

ISOLATION AND DIAGNOSTIC DATA COLLECTION

Serial No.:

Examiner:

10/697,365

October 30, 2003 Edward Raymond

Group Art Unit: 2857

Transmitted berewith is:

Filed:

X

Amendment

X Transmittal and Fax Cover Sheet

FEE CALCULATION					
Fee Items	Highest No. of claims previously paid	Present No. of claims filed	Extra Claims	Fee Rate	Total
Total Claims	20	N/A		X \$18.00	\$0.00
Independent Claims	- 3	N/A		X \$86.00	\$0.00
Basic Filing Fee \$770.00					PAID
TOTAL FEES					\$0.00

The Commissioner is hereby authorized to charge \$0.00 to Deposit Account No. 09-0456.

The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to $\mathbf{X}\mathbf{X}$ Deposit Account No. 09-0456. A duplicate copy of this transmittal is enclosed.

XXPlease address all future correspondence to:

Brian M. Dugan Dugan & Dugan, PC 55 South Broadway Tarrytown, NY 10591

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BRIAN M. DUGAN

(name of person certifying)

PAGE 2/7 * RCVD AT 10/18/2004 7:05:37 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/0 * DNIS:8729306 * CSID:9143329082 * DURATION (mm-ss):03-34

October 18, 2004

Date

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PATENTS BUR920010034US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants

Leendert M. Huisman, Franco Motika, Leah M.P.

Pastel and William V. Huott

Serial No.

10/697,365

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Filed

: October 30, 2003

For

USING CLOCK GATING OR SIGNAL GATING TO

PARTITION A DEVICE FOR FAULT ISOLATION AND

DIAGNOSTIC DATA COLLECTION

Examiner

Edward Raymond

Group Art Unit :

2857

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION

Sir:

This correspondence is responsive to the Office Action mailed July 19, 2004.

Remarks/Arguments begin on page 2 of this paper.

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October 18, 2004 Date

BRIAN M. DUGAN (name of person certifying)